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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of: )  
PASOTTI ET AL. )  
 ) Examiner: J. ZWEIZIG  
Serial No. 10/748,697 )  
Confirmation No. 2009 ) Art Unit: 2816  
Filing Date: DECEMBER 30, 2003 )  
 ) NOA Date: 01/25/05  
For: VOLTAGE SUPPLY DISTRIBUTION )  
ARCHITECTURE FOR A PLURALITY OF )  
MEMORY MODULES )

TRANSMITTAL OF FORMAL DRAWINGS

Mail Stop Issue Fee  
Commissioner for Patents  
Alexandria, VA 22313-1450

Sir:

Enclosed are two (2) sheets of formal drawings to be  
filed in the above-identified patent application.

Respectfully submitted,

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CERTIFICATE OF MAILING

I hereby certify that this correspondence is being  
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mail in an envelope addressed to: COMMISSIONER FOR PATENTS,  
ALEXANDRIA, VA 22313-1450, on this 9<sup>th</sup> day of March, 2005.